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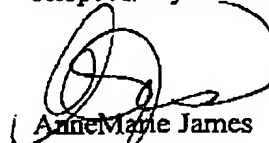
Applicant:	Chen, et al.	Docket No.:	TSM03-0323
Serial No:	10/676,703	Patent No.:	6,924,531 B2
Date Filed:	October 1, 2003	Date Issued:	August 2, 2005
Title:	LDMOS Device with Isolation Guard Rings		

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<b>CHANGE OF CORRESPONDENCE ADDRESS Patent</b>  Address to: Mail Stop Post Issue Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	Patent Number	6,924,531 B2
	Issue Date	8/2/2005
	Application Number	10/676,703
	Filing Date	10/1/2003
	First Named Inventor	Chen, et al.
	Attorney Docket Number	TSM03-0323

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